OIPE IAP100
NOV 09 2006
PATENT & TRADEMARK OFFICE

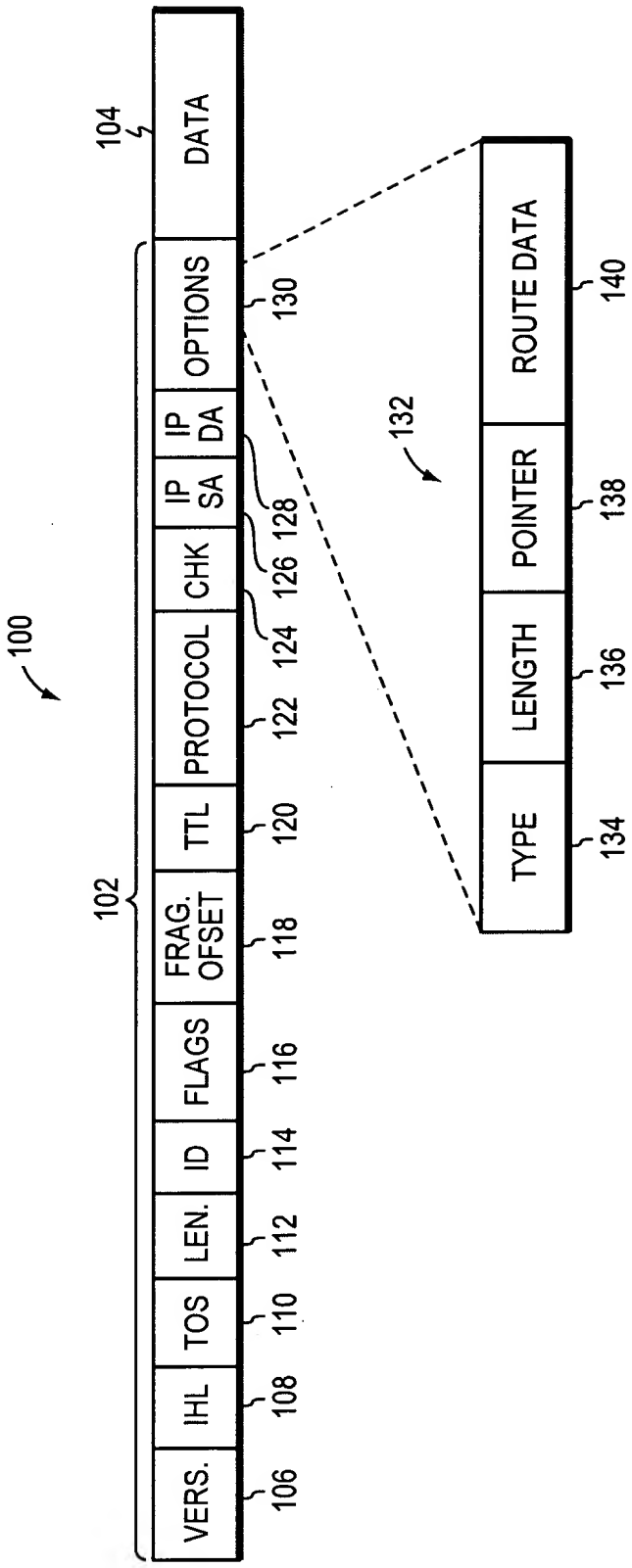

FIG. 1A
(PRIOR ART)

ANNOTATED
SHEET

2/11

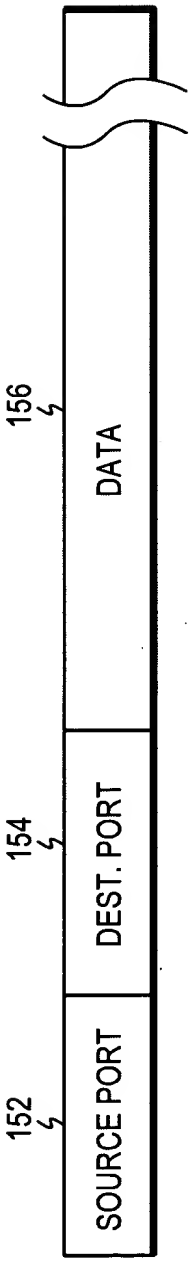

FIG. 1B
(PRIOR ART)

ANNOTATED
SHEET

3/11 7

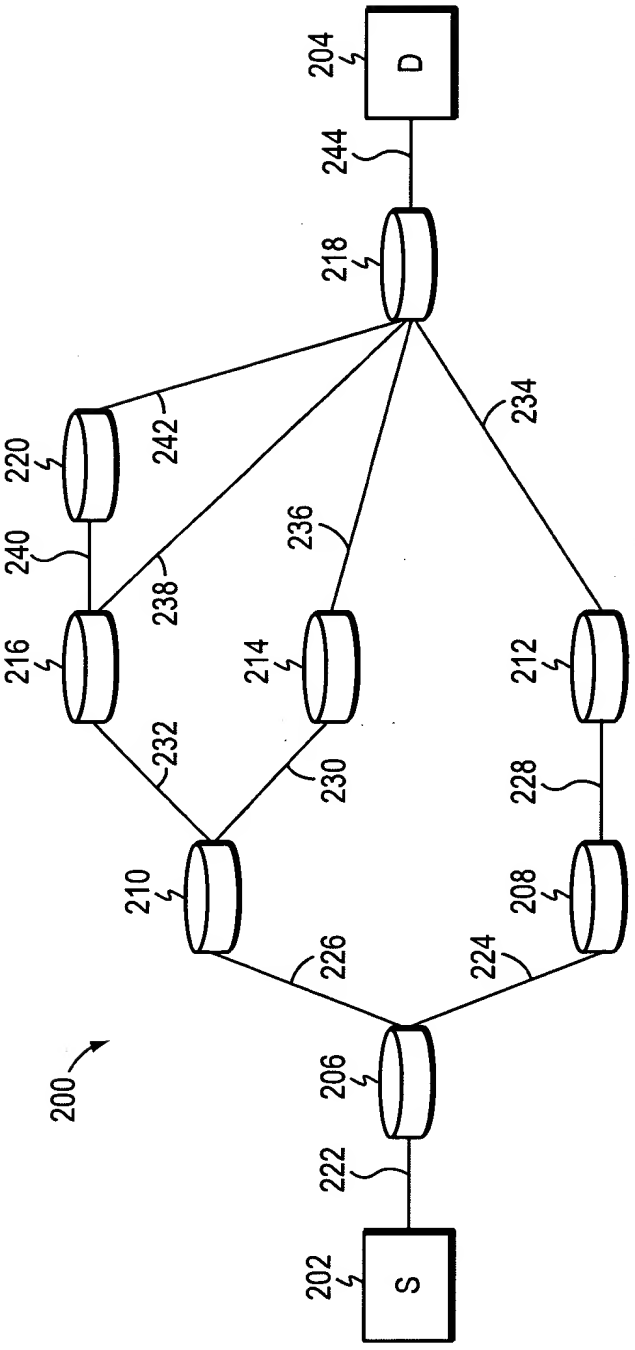

FIG. 2

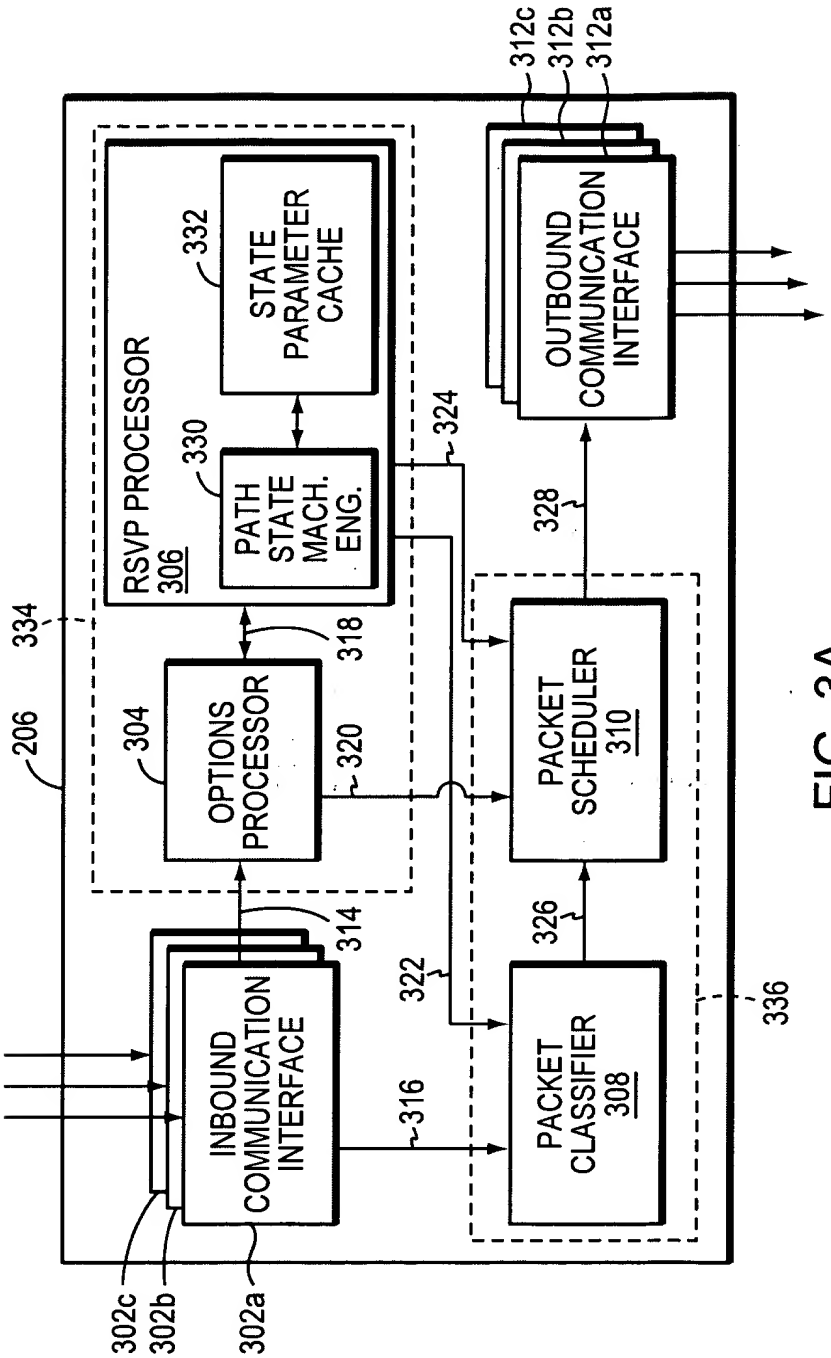
312c
312b
312a
OUTBOUND COMMUNICATION INTERFACE
332
STATE PARAMETER CACHE
RSVP PROCESSOR 306
330
PATH STATE MACH. ENG.
324
328
334
318
206
304
OPTIONS PROCESSOR
320
PACKET SCHEDULER 310
314
326
322
336
INBOUND COMMUNICATION INTERFACE
316
PACKET CLASSIFIER 308
302c
302b
302a

FIG. 3A

ANNOTATED
SHEET

7

5/11

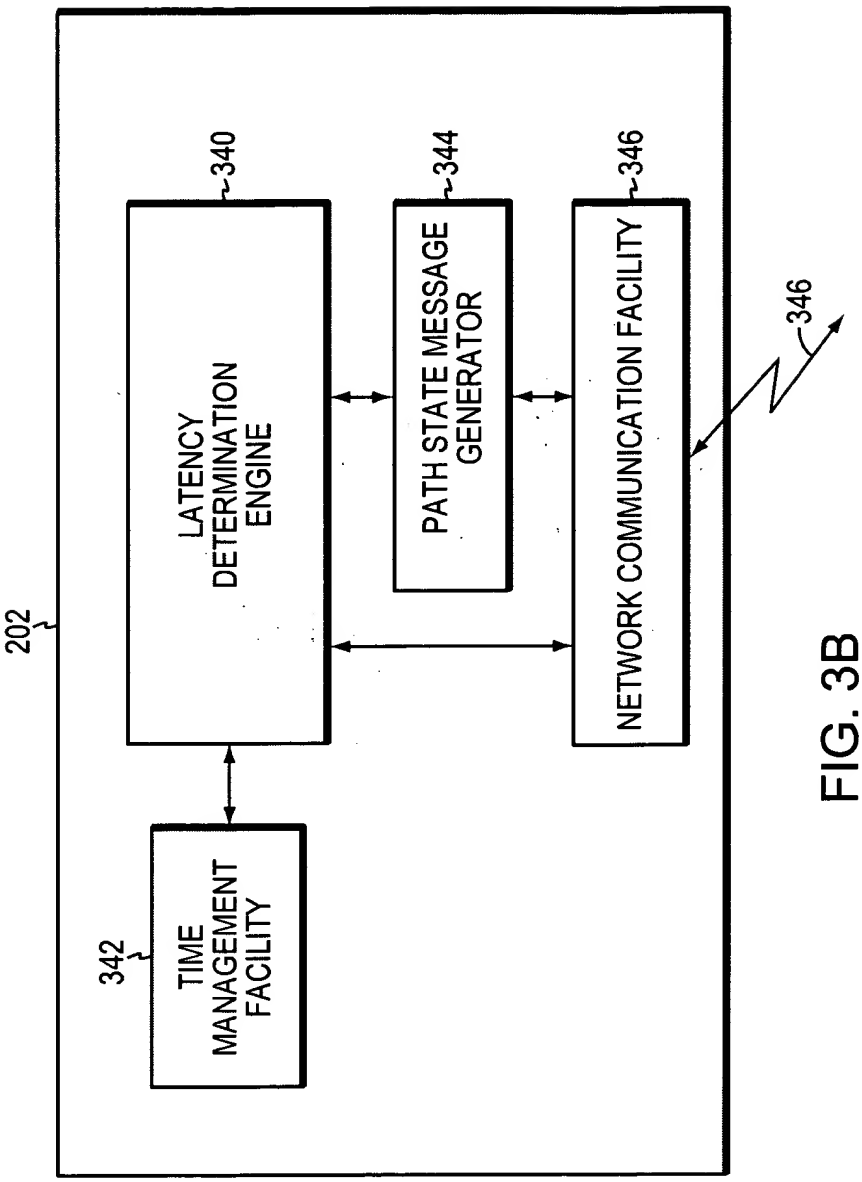

FIG. 3B

ANNOTATED SHEET

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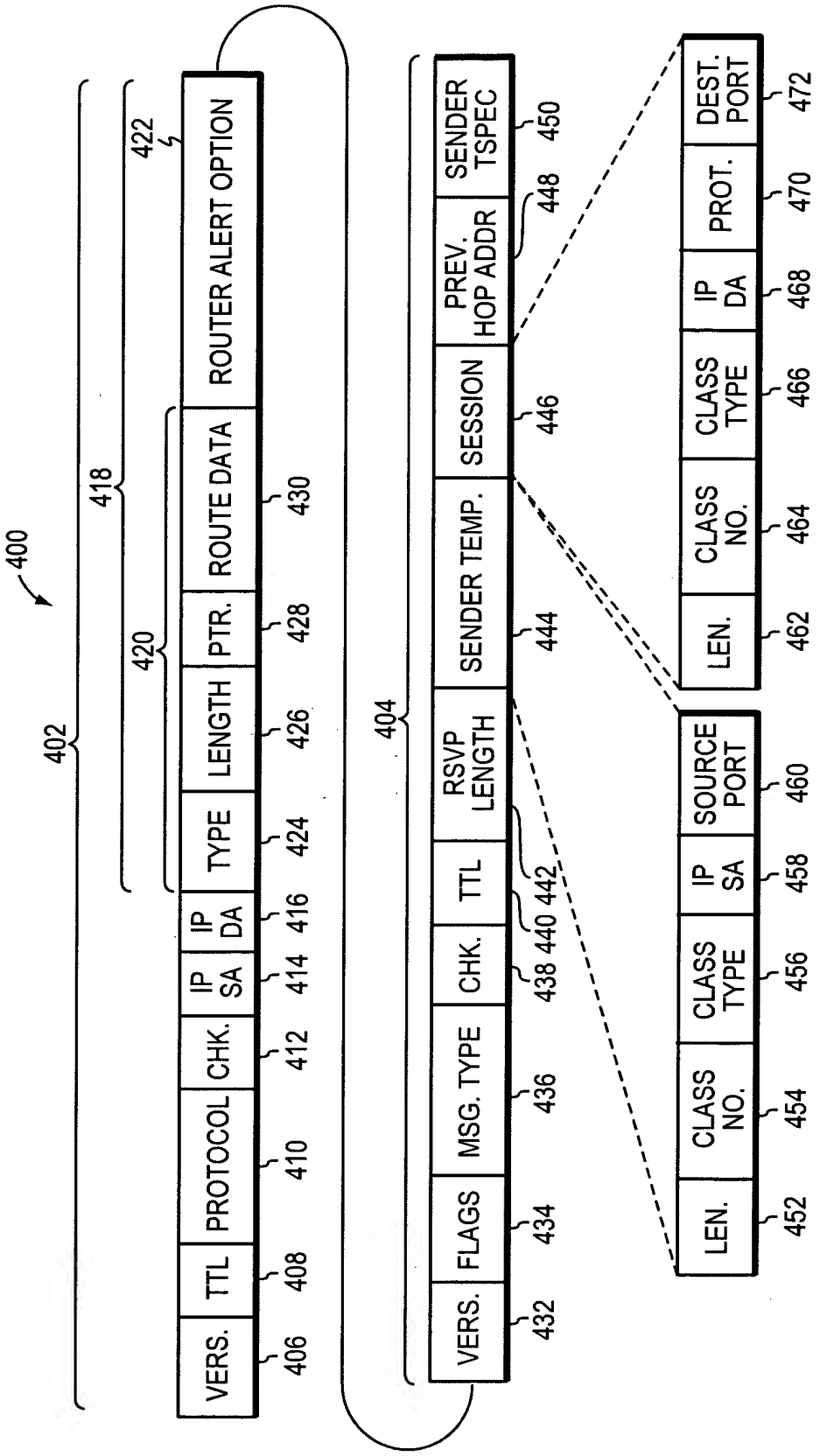

FIG. 4

ANNOTATED SHEET

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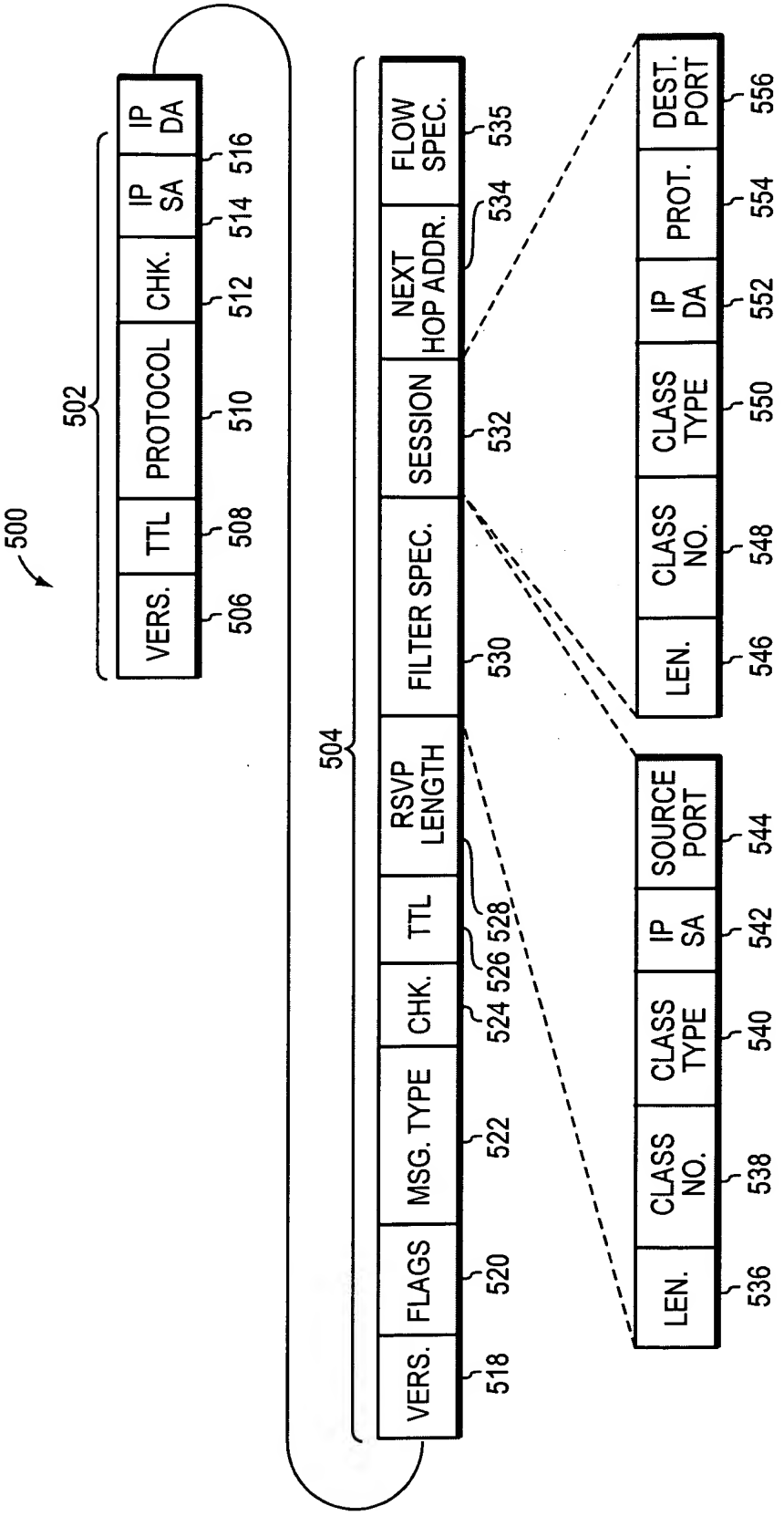

FIG. 5